

2818

Docket No.: W&B-INF-1910

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By: _____

Date: April 12, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/667,256 Confirmation No: 1145
Applicant : Dirk Fuhrmann et al.
Filed : September 19, 2003
Title : Test Circuit of an Integrated Memory Circuit for Coding
Assessment Data and Method for Testing the Memory Circuit
Art Unit : 2818
Examiner : to be assigned

Docket No. : W&B-INF-1910
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 6,359,820 B2 (Daehn et al.), dated March 19, 2002, and corresponding German Published Non-Prosecuted Patent Application DE 100 16 719 A1, dated October 11, 2001;

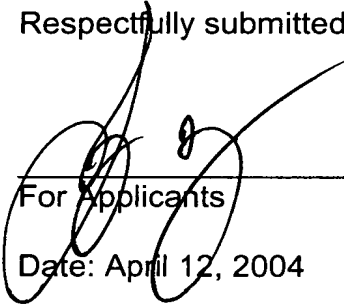
Kenneth C. Smith: "Multiple-Valued Logic: A Tutorial and Appreciation", *IEEE*, 1988, pp. 17-27;

German Examination Report dated March 18, 2004.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant. As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,



For Applicants

Date: April 12, 2004

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/bmb



FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Attorney Docket No.: W&B-INF-1910	Applic. No. 10/667,256
	Applicant Dirk Fuhrmann et al.	
	Filing Date September 19, 2003	Group Art Unit 2818

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,359,820 B2	03/19/02	Daehn et al.			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO	
	J	100 16 719 A1	10/11/01	Germany				X
	K							
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	Kenneth C. Smith: "Multiple-Valued Logic: A Tutorial and Appreciation", <i>IEEE</i> , 1988, pp. 17-27
	P	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.